

IN THE CLAIMS

Please cancel Claim 17 without prejudice and amend Claim 15 as follows:

1. (Original) A variable gain amplifying stage in a transmitter chip, said variable gain amplifying stage comprising:

a first circuit operable to provide a current drive signal in response to a reception of a voltage control signal and voltage intermediate frequency signal by said variable gain amplifying stage, said current drive signal having an AC current component and a DC current component;

a second circuit operable to provide a DC current control signal in response to a reception of said voltage control signal by said variable gain amplifying stage; and

wherein a ratio of a first ampere level of said DC current component of said current drive signal to a second ampere level of said DC current control signal is constant.

2. (Original) The variable gain amplifying stage of claim 1, wherein said second circuit is a replica of said first circuit.

3. (Original) The variable gain amplifying stage of claim 1, wherein means for adjusting said ampere level of said DC current

component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a voltage level of said voltage control signal.

4. (Original) The variable gain amplifying stage of claim 1, wherein means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a temperature of the transmitter chip.

5. (Original) The variable gain amplifying stage of claim 1, wherein means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a processing performance of the transmitter chip.

6. (Original) The variable gain amplifying stage of claim 1, wherein means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a supply power of said transmitter chip.

7. (Original) A transmitter chip, comprising:

a variable gain amplifying stage operable to provide a current drive signal and a DC current control signal, said DC current drive signal having an AC current component and a DC current component;

a biasing stage operable to provide a first DC current biasing signal in response to a reception of said DC current control signal; and

wherein a first ratio of a first ampere level of said DC current component of said current drive signal to a second ampere level of said DC current control signal is constant.

8. (Original) The transmitter chip of claim 7, wherein said variable gain amplifying stage includes means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a voltage level of said voltage control signal.

9. (Original) The transmitter chip of claim 7, wherein said variable gain amplifying stage includes means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a temperature of the transmitter chip.

10. (Original) The transmitter chip of claim 7, wherein said variable gain amplifying stage includes means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a processing performance of the transmitter chip.

11. (Original) The transmitter chip of claim 7, wherein said variable gain amplifying stage includes means for adjusting said ampere level of said DC current component of said current drive signal and said ampere level of said DC current control signal in response to any variation in a supply power of said transmitter chip.

12. (Original) The transmitter chip of claim 7, wherein a second ratio of said second ampere level of said DC current control signal to a third ampere level of said first DC biasing current signal is constant.

13. (Original) The transmitter chip of claim 7, further comprising:

a phase shifting stage operable to provide a current intermediate frequency signal in response to a reception of said DC current drive signal and said first DC biasing current signal.

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14. (Original) The transmitter chip of claim 13, further comprising:

a mixing stage;

wherein said biasing stage is further operable to provide a second DC biasing signal in response to a reception of said DC current control signal; and

wherein said mixing stage is operable to provide a current radio frequency signal in response to a reception of said current intermediate frequency signal and said second DC current biasing signal.

15. (Currently Amended) The transmitter chip of claim 414, wherein a second ratio of said second ampere level of said DC current control signal to a third ampere level of said second DC biasing current signal is constant.

16. (Original) The transmitter chip of claim 14, wherein a gain of said mixing stage is constant.

17. (Cancelled)